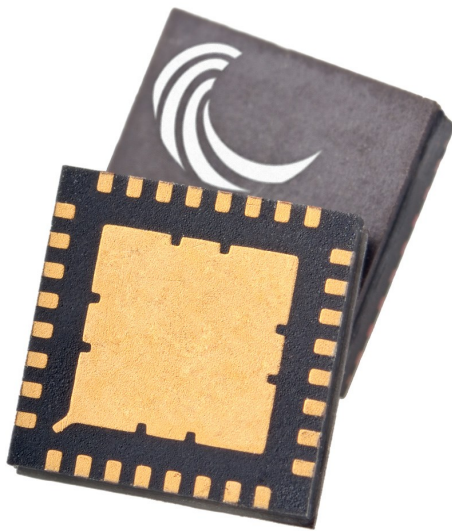


## LW48-700151 QFN Dual Stage PIN Limiter



### Typical Applications

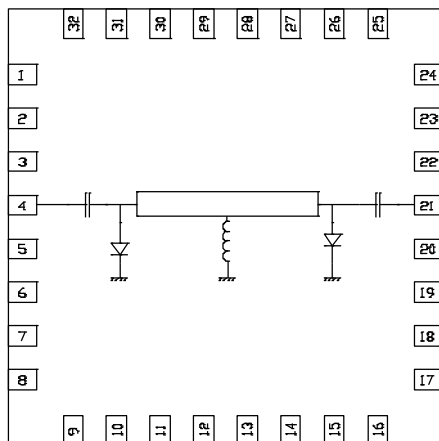
- LNA receiver chain protection
- Radar receiver protection

### Features

- 100-3000MHz Passive, high isolation limiter
- Low loss < 0.8dB
- Return Loss > 15dB
- Flat Leakage < +18dBm
- Input Power CW Survivability >10W
- Integrated DC Block on both input and output
- QFN dimensions 5.0 x 5.0 x 1.6 mm, 32 lead

### General Description

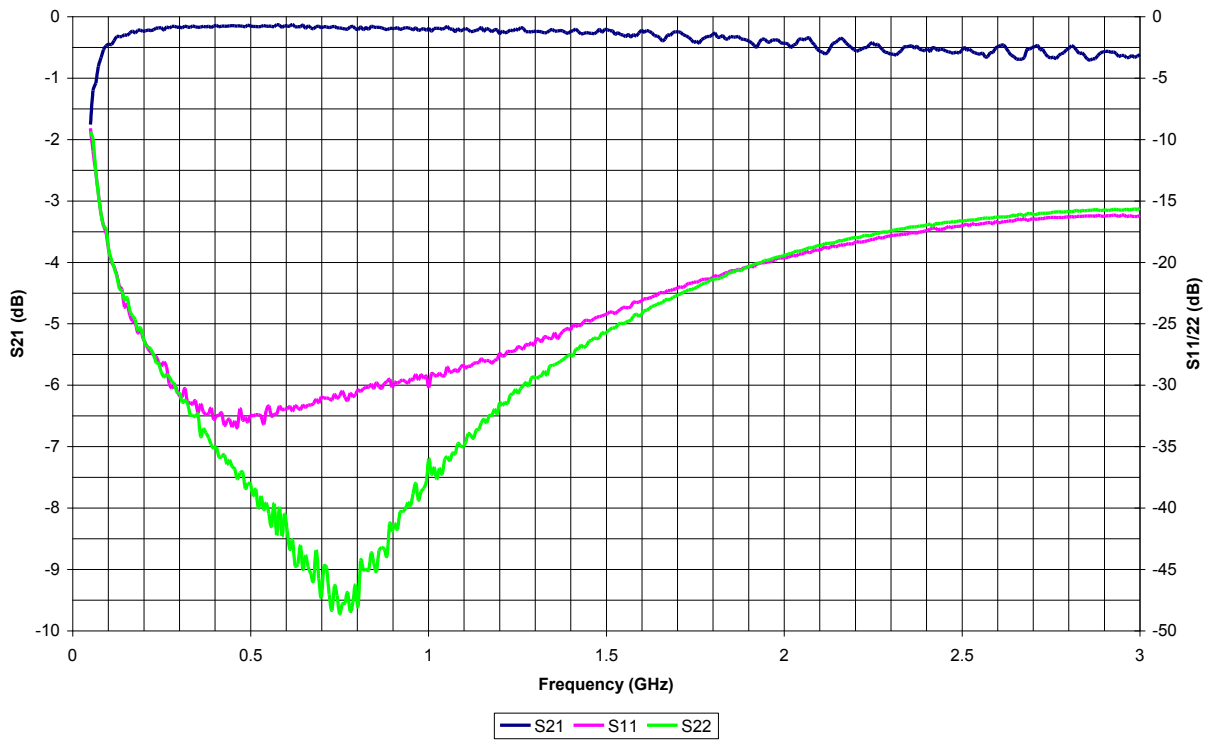
The LW48-700151 is an ultra-wideband two stage PIN diode limiter packaged in a leadless 5x5 mm surface mount package which operates between 100 and 3000 MHz. The limiter provides flat leakage of <+18dBm, return loss of >15dB with typical insertion loss of 0.5dB



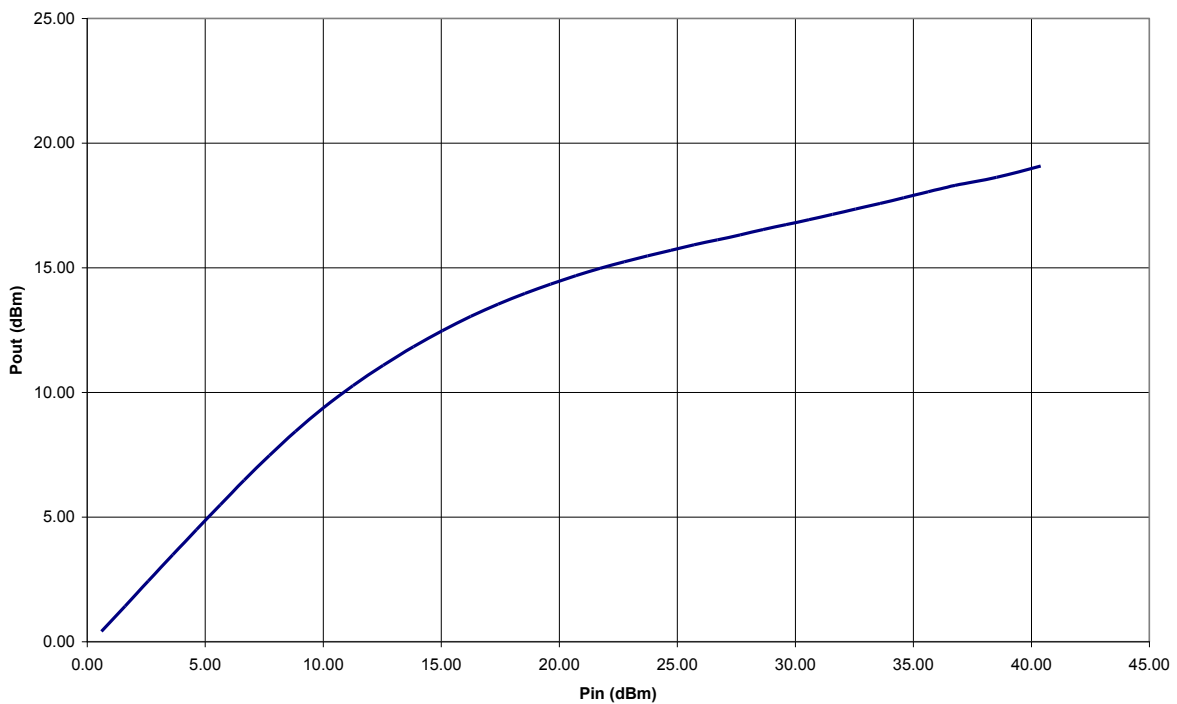
### Pin Designations

Pin No.	Function
Pin 4	RF IN
Pin 21	RF OUT
Pin 1-3, 5-20	GROUND
Pin 22-32	GROUND

## Insertion Loss and Return Loss



## Limiting Characteristics @ 100MHz



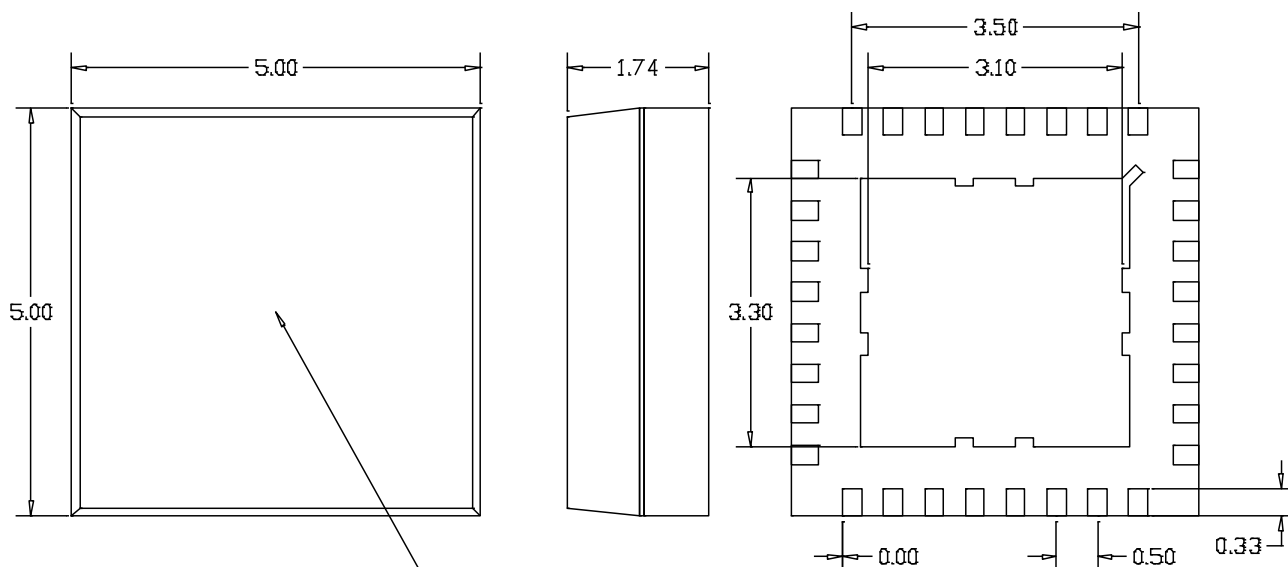
**TABLE I**  
**ABSOLUTE MAXIMUM RATINGS**

Symbol	Parameter	Value
P <sub>IN</sub>	Input CW Power	+42dBm
T <sub>M</sub>	Mounting Temperature (30 secs)	260°C
T <sub>STG</sub>	Storage Temperature	-55 to +125°C
T <sub>OP</sub>	Operating Temperature	-40 to +85°C

**TABLE II**  
**RF CHARACTERISTICS**  
**(T<sub>A</sub> = 25°C)**

Symbol	Parameter	Test Condition	Limit			Units
			Min	Typ	Max	
F	Frequency Range	Swept Frequency	100		3000	MHz
IL	Insertion Loss	Swept Frequency		0.5	0.8	dB
IRL	Input Return Loss	Swept Frequency	15			dB
ORL	Output Return Loss	Swept Frequency	15			dB
PWR	Output Power @ Pin = +40dBm	F=100MHz		18		dBm
P <sub>cw</sub>	CW Incident Power	Swept Frequency			10	W
P <sub>pulse</sub>	Peak Incident Power	1µs pulse width, 10% duty cycle			100	W
P1dB	Threshold Power	Swept Frequency		+11		dBm
Pf	Flat Leakage Power	Swept Frequency, +10dBm CW		+18		dBm
Es	Spike Leakage Energy	+50dBm, 1µs pulse, 10% duty		0.2		ergs
Tr	Recovery Time	+50dBm, 1µs pulse, 10% duty 50% trailing RF Pulse – 1dB IL)			50	
ILtemp	Insertion Loss Rate of Change with Operating Temperature				-0.005	dB/°C

## Outline Drawing



MANUFACTURERS  
LABELLING ON THIS  
FACE

### NOTES

- 1> BODY: PLASTIC, SEMICONDUCTOR GRADE  
LID: LCP
- 2> LEAD FRAME: COPPER, 194 FH
- 3> LEAD FINISH: FULL GOLD PLATE ON FOOTPRINT  
(1.27 $\mu$ m Au OVER 0.76 $\mu$ m Ni)  
SIDE CONTACTS NON-PLATED
- 4> FRAME THICKNESS: 0.2030  $\pm$  0.0076
- 5> EXTERNAL DIMENSIONS  $\pm$  0.15

**Refer to Linwave application note for suggested PC Board Land Pattern.**



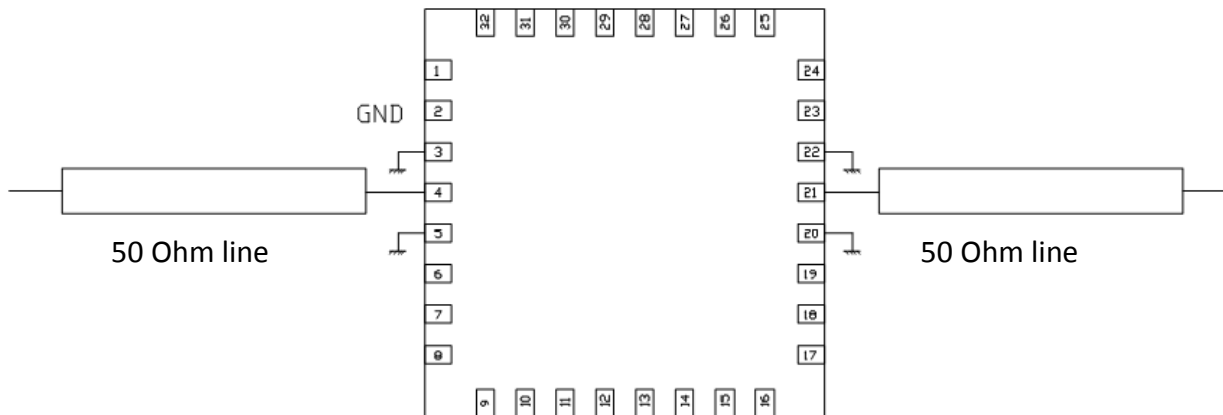
**ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS**

## Pin Descriptions

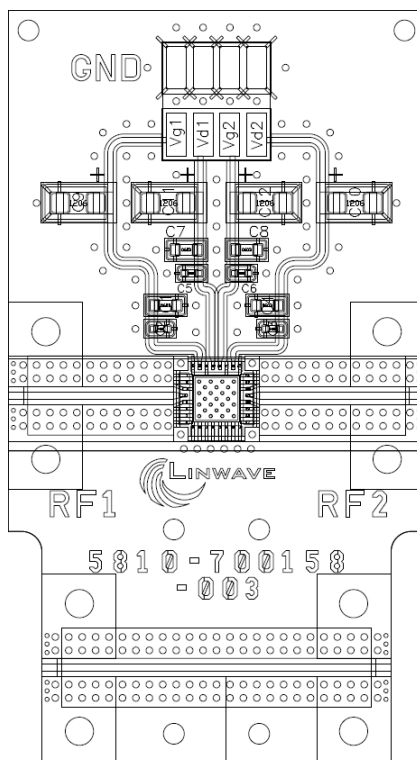
Pin Number	Function	Description
4	RF IN	This pad is AC coupled and matched to 50 ohms
21	RF OUT	This pad is AC coupled and matched to 50 ohms
1,2, 6-19, 23-32	N/C	The pins are not connected internally; however, all data shown was measured with these pins connected to RF/DC ground externally.
3,5,20,22	GROUND	Must be connected to RF/DC ground
Ground paddle	GROUND	Must be connected to RF/DC ground

## Application Circuit

Note: Effective heatsinking through the ground paddle on the underside of the package is essential for high power operation (RF Input >1W)



## Evaluation PCB



The circuit board used in the application should use RF circuit design techniques. The signal lines should have 50 ohms impedance and the package ground leads and package bottom should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Linwave upon request.

### List of Materials for Evaluation PCB LW54-700151<sup>[1]</sup>

Item	Description
J1-J2	Southwest Microwave 292-06A-5
U1	LW48-700151 Limiter
PCB <sup>[2]</sup>	5810-700158-003

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit board material: Rogers 4350B on FR4 backing